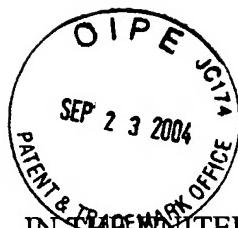


IT0011-US



JPW

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

MING-DOU KER, ET AL.

Serial No.: 10/626,601

Art Unit: 2819

Filed: July 25, 2003

Examiner: Not Yet Assigned

For: MIXED-VOLTAGE CMOS I/O
BUFFER WITH THIN OXIDE
DEVICE AND DYNAMIC N-WELL
BIAS CIRCUIT

STATUS INQUIRY

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicant respectfully inquires as to the status of the above-identified patent application, and when the next communication from the United States Patent and Trademark Office regarding this application can be expected.

SHAW PITTMAN LLP
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Date: September 23, 2004

Respectfully submitted,

MING-DOU KER, ET AL.

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